REMARKS

This application has been amended so as to place it in condition for allowance at the time of the next Official Action.

The Official Action rejects claims 1-8 under 35 USC §101 for reciting an invention directed to non-statutory subject matter.

Applicant has amended independent claims 1 and 5 and the claims that depend therefrom. Each of the independent claims now recites, as a first step, providing the various structure necessary to carry out the steps of the method. Each of the method steps also now recites the structure used to execute that step.

Accordingly, the claim now recites structure and the actions taken by such structure as method steps. The invention recited is believed to be statutory, and reconsideration and withdrawal of this rejection are therefore respectfully requested.

The Official Action rejects claims 1-18 under 35 USC §103 as being unpatentable over applicant's illustration of the prior art in Figure 1 of the present application in view of JP 7-30373. Reconsideration and withdrawal of this rejection are respectfully requested for the following reason:

A significant difference between the teachings of the applied reference and the present invention as claimed is the kind of number that is processed in the calculating elements.

According to the present invention, the unit angle U is expressed as A + C/B, where A, B, and C are natural numbers, and where A > 1, B > C. In the periodic function generating circuit of the present invention, a decimal fraction is expressed as a fractional number C/B, and the integers B and C are processed in the section 120.

For example, with reference to Fig. 5 of the present application, the adder 3, the subtractor 6 and the adder 8 process only natural numbers, as is recited in the present claims. No decimals are processed at these elements. The comparison between the value B with the multiple ΣC is also made on an integer basis.

Therefore, the adders and the subtractor of the present invention are only required to process natural numbers. This vastly simplifies the requirements for these elements. According to the present invention, the power consumption and size parameters of a periodic function generating circuit can be reduced.

On the other hand, according to the citation, the integrating means (3) processes a decimal fraction as a decimal fraction. The citation does not disclose how to process a decimal fraction with integer calculators. Therefore, it lacks the
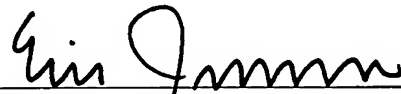
ability to perform the present method as recited, with N, A, B, and C as natural numbers.

In light of the amendments described above and the arguments offered in support thereof, applicant believes that the present application is in condition for immediate allowance and an early indication of the same is respectfully requested.

The Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 25-0120 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17.

Respectfully submitted,

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EJ/mjr
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